APR 0 6 1998

VALUE ONITED STATES PATENT AND TRADEMARK OFFICE

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In re application of: FRANK R. BRYANT ET AL

Serial No.: 08/712,808

Filed: September 12, 1996

For: STRUCTURE FOR

TRANSISTOR DEVICES IN AN SRAM CELL

Group Art Unit: 2503 RECEIVE

- - A

Examiner: N. Ngo

APR 1 0 1998

S Attorney Dkt. No.: 96-c-CAROUP 2200

Certificate of Mailing Under 37 C.F.R. § 1.8(a)

I certify that this correspondence is being deposited with the United States Postal Service as First Class mail in an envelope with sufficient postage addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on April 3, 1998.

Washington, D.C. 20231 on April 3, 1996.

Debbie Smith

## AMENDMENT AND RESPONSE TO OFFICE ACTION

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

A one month extension of time is believed to be required. An appropriate petition and fee are enclosed.

No additional fees are believed to be necessary; however, in the event that any additional fees are required for the prosecution of this application, please charge any necessary fees to Deposit Account No. 06-0580. No additional extension of time is believed to be necessary. If, however, an additional extension of time is needed, the extension is requested and please charge the fee for this extension to Deposit Account No. 06-0580.

In response to the Office Action dated December 4, 1997, please amend the above-identified application as follows:

## IN THE CLAIMS:

Please amend the claims as follows:

1. (amended) An SRAM memory cell comprising:

first and second transfer gate transistors, the first transfer

1004KH-25447/54450.1

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